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**PATENT NUMBER and
ISSUE DATE**

U.S. UTILITY Patent Application

APPL NUM	FILING DATE	CLASS	SUBCLASS	GAU	EXAMINER
10065626	11/04/2002	365	230.06	2818	HOANG, HUAN

****APPLICANTS:** Atallah Francois; Burda Gregory; Corræale Anthony;

**CONTINUING DATA VERIFIED

**** FOREIGN APPLICATIONS VERIFIED:**

PG-PUB	DO NOT PUBLISH <input type="checkbox"/>	RESCIND <input type="checkbox"/>	
Foreign priority claimed <input type="checkbox"/> yes <input type="checkbox"/> no		ATTORNEY DOCKET NO	
35 USC 119 conditions met <input type="checkbox"/> yes <input type="checkbox"/> no		RPS920020085	
Verified and Acknowledged Examiners's initials			
TITLE : Method and circuit for optimizing power consumption and performance of driver circuits			
U.S.DEP'T. OF COMM./PAT.& TM-PTO-436L(Rev. 12-9)			

NOTICE OF ALLOWANCE MAILED		Assistant Examiner	CLAIMS ALLOWED		
			Total Claims	Print Claim for O.G.	
ISSUE FEE		DRAWING			
Amount Due	Date Paid	Sheets Drawn	Fig's Drawn	Print Fig.	
TERMINAL		Primary Examiner			Application Examiner
DISCLAIMER		<p>WARNING: The information disclosed herein may be restricted. Unauthorized disclosure may be prohibited by the United States Code Title 35, Sections 122, 181 and 368. Possession outside the U.S. Patent & Trademark Office is restricted to authorized employees and contractors only.</p>			

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